

Patent

Attorney's Docket No.: P18244

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Patent Application of:

J. Brask et al.

Application No.: 10/748,559

Filed: December 29, 2003

**Title: A CMOS DEVICE WITH METAL
AND SILICIDE GATE ELECTRODES AND
A METHOD FOR MAKING IT**

Examiner: T. Le

Art Unit: 2814

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22131-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

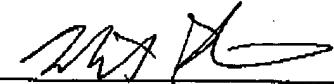
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Respectfully submitted,

Dated: May 2, 2006


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				Application Number	P18244
				Filing Date	December 29, 2003
				First Named Inventor	J. Brask et al.
				Art Unit	2814
				Examiner Name	Le, Thoa X
Sheet	1	of	1	Attorney Docket Number	P18244

NON-PATENT LITERATURE DOCUMENTS

Examiner Signature		Date Considered	
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***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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